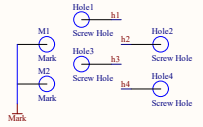
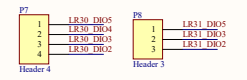
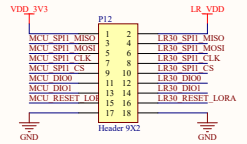


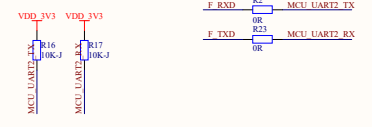
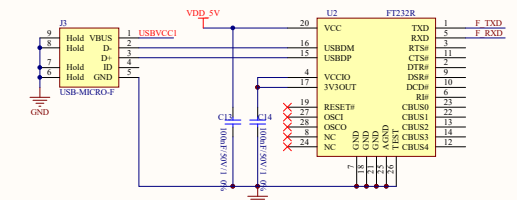
$$V_{out} = 0.6 (1 + R1/R2)$$



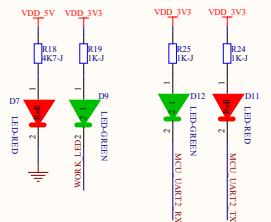
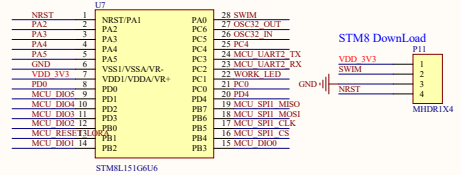
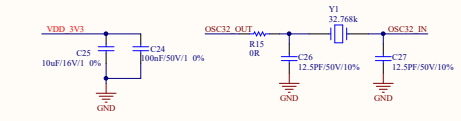
# 1 POWER



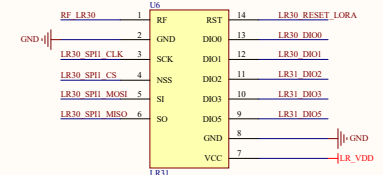
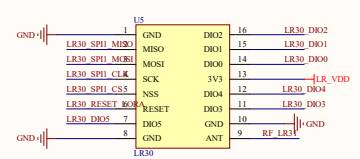
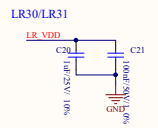
# 2 PORT



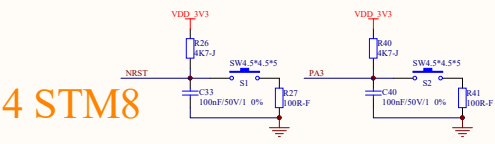
# 3 USB



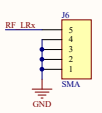
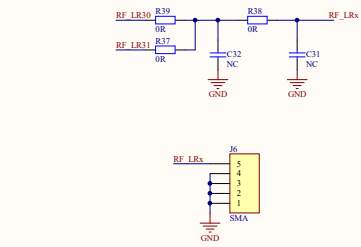
# 4 STM8



# 6 LORA



# 5 LED



# 7 SMA

Title		
Size	Number	Revision
Date: 2019/5/27		
File: P:\...WHL4LR30-1-EVK-V1.1_SchDoc1.Dwg		